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configuration memory data

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... possible because of the device's 16-Mbit flash chip and incorporated **data** compression/decompression scheme that effectively doubles the **configuration memory**. ...

www.altera.com/products/devices/config/features/cfg-adv_features.html - 34k -

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Patent 4617624: Multiple configuration memory circuit

... In the second **configuration**, the **memory** provides a series of **data** words in accordance with the single address and an address incrementing signal. Assignee: ...

www.freepatentsonline.com/4617624.html - 52k - [Cached](#) - [Similar pages](#)

EDN - CompactFlash modules boost configuration-memory densities ...

... 144-lead-TQFP **configuration** controller automatically pulls a bit-stream file from a CompactFlash **memory** card or IBM MicroDrive and transforms the **data** into a ...

www.edn.com/article/CA85657.html - [Similar pages](#)

Changes to Functionality in Microsoft Windows XP Service Pack 2 ...

... sections of the image file loaded in **memory** as non ... System-wide **Configuration** of DEP. **Data** execution prevention **configuration** for the system is controlled through

...

www.microsoft.com/technet/prodtechnol/winxp/maintain/sp2mempr.mspx - 46k -

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[PDF] FPGA Configuration Memory AT17N256 AT17N512 AT17N010 AT17N002 ...

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... Mode • High-reliability – Endurance: Minimum 10 Write Cycles – **Data** Retention: 20

... provide an easy-to- use, cost-effective **configuration memory** for Field ...

www.atmel.com/dyn/resources/prod_documents/doc3020.pdf - [Similar pages](#)

Config::FreeForm - Provide in-memory configuration data

Config::FreeForm - Provide in-memory **configuration data**. ... DESCRIPTION.

Config::FreeForm provides in-memory **configuration data** in a free-form manner. ...

stupidfool.org/perl/docs/perl/doc/Config/FreeForm.html - 10k - [Cached](#) - [Similar pages](#)

Tech Tips FPGA Configuration Glossary

... The **configuration memory** contains the **configuration data**, allowing for a Read-Verification of the **configuration data**, and can also contain the logic states ...

www.xilinx.com/xlnx/xil_tt_glossary.jsp?BV_UseBVCookie=yes&sProduct=FPGA+Configuration - 36k -

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[PDF] Secure Configuration of a Field Programmable Gate Array

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... into the FPGA using the JTAG interface during manufacturing test and have the

FPGA itself program the information into the external FLASH **configuration memory**.

...

www.algotronix.com/content/security%20fccm01.PDF - [Similar pages](#)

[The Code Project - Managing **configuration** settings persistence in ...](#)

... System.IO ' Class for managing **configuration** persistence Public Class ConfigOpt '

This DataSet is used as a **memory data** ' structure to hold config key/value ...

www.codeproject.com/vb/net/ConfigOpt.asp - 50k - [Cached](#) - [Similar pages](#)

DB2 Help

... **Configuration** Parameter Summary; Database **Configuration** Parameter Summary;

... Database Manager Instance **Memory**; Locks; I/O ... Enabler; Attributes; DB2 Data

Links Manager ...

publib.boulder.ibm.com/infocenter/

db2v7luw/topic/com.ibm.db2v7.doc/db2help/nldb2h03.htm - 35k -

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